

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0004] with the following:

[0004] The ability to access data having different data widths may result in unaligned data. As illustrated in FIG. 1, memory system 120 contains data sets A, B, C, D, E, and F. Each data set is separated as one or more half words in memory system 120. For example, data set A includes half words A1, A1, A2, and A3. Data set B includes half word B0. Data set C includes half words C0 and C1. Data set D includes half words D0, D1, D2, and D3. Data set E includes half word E0 and E1. Data set F includes half words F1, F2, F3, and F4 (not shown). Data set A, which is located completely in row 0, is aligned data and can easily be retrieved in one memory access. However, data set D is located in both row 1 and row 2. To retrieve data set D, a conventional CPU 110 must access memory system 120 twice. First to retrieve half word D0 in row 1 and then to retrieve half words D1, D2, and D3 in row 2.